

Amendments to the Specification

Please amend the paragraph beginning on page 10, line 31 as follows:

The phase combining circuit 103a receives, for example, four-phase clocks (reference clocks) Φ_0 , Φ_{0X} , Φ_1 , and Φ_{1X} and the outputs of the D/A converter 103d 103b, applies weights WS (the outputs of the D/A converter 103d 103b) to the respective reference clocks, and thereby outputs through the amplifying circuit 103c a clock Θ (Θ_X) having a phase intermediate between the respective reference clocks. Here, the four-phase clocks are of the same frequency but differ in phase by 90° relative to each other; that is, the clock Φ_0 has a phase difference of 180° relative to the clock Φ_{0X} , and likewise, the clock Φ_1 has a phase difference of 180° relative to the clock Φ_{1X} .